This is revision C of the DualMotor1Amp module. The status of this project is work in progress.

# **DualMotor1Amp Module (Revision C)**

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### 1. Introduction

The DualMotor1Amp module allows for control of up to two small DC motors via pulse width modulation. Power is provided via an external terminal block. For low voltage motors, an on–board 5 volt voltage regulator can be optionally used to limit the motor voltage to 5 volts.

# 2. Programming

The DualMotor1Amp module can control up to two motors called 0 and 1 respectively. Each motor has a condition, power mode, direction, and speed. The condition is either on or off (i.e. freewheel.) The two power modes are pulsed and continuous. The two directions are forward and backward. The speed is a number between 0 and 255 inclusive. There is one additional variable associated with each motor called ramp and a few additional variables that are shared between the two motors.

Pulsed mode is standard motor control via pulse width modulation (PWM.) When the speed is 0, no pulses are sent to the motor. When the speed is 255, the motor is full on. When the speed is 128, 50% duty cycle pulses are sent to the motor. The direction bit, controls what direction current is pulsed into the motor.

In continuous mode, power is continuously applied either forward or backward through the motor. In continuous mode, when the speed is 128, 50% duty cycle pulses are sent to the motor, where half the cycle is sends current forward through the motor and the other half is sends current backward through the motor (thereby cancelling out and resulting in a rotational speed of 0.) While continuous mode consumes more power than pulsed mode, it sometimes provides better motor speed control at slow speeds.

The ramp variable is used to slow down the rate at which motor speeds are changed. When the ramp variable is non-zero, it specifies the rate at which motor speed changes (i.e. the speed ramp.) The ramp rate is measured in ticks (1/3 of a bit time at 2400 baud, or 1/7200, or 138 $\mu$ S. A ramp rate of 1, means the pulse widths will be changed every 138 $\mu$ S. A ramp rate of 100 means the pulse widths will be changed every 100 × 138 $\mu$ S or every 13.8mS. This allows the user to slowly speed up and slow down the motor. Please note, that ramp only applies to speed, changing the motor direction is immediate. (Sorry!)

For safety reasons, you might want the motors to shut off if the controlling program crashes. This is

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accomplished with a variable called the failsafe delay variable which is shared between both motors. When the failsafe delay variable is set to a non–zero value, it causes another variable called the failsafe counter to be initialized to the same value. Every 256 ticks (=  $256 \times 138 \mu S = 35.5 mS$ ), the failsafe counter is decremented. If the failsafe counter ever decrements to 0, it immediately turns off both motors without any ramping. Every time a speed command is sent to the DualMotor1Amp module, the failsafe counter is reinitialed to contain the failsafe delay variable. Thus, by occasionally sending commands that set the speed of either motor, the failsafe counter can be kept non–zero. Alternatively, there is a command that just reinitializes the failsafe counter without affecting the speed. The maximum amount of time between commands that reset the failsafe counter is  $255 \times 35.5 mS$  or approximately 9 seconds. If the controlling program crashes, it will stop sending commands to the DualMotor1Amp module and eventually, the failsafe counter will decrement to zero and stop both motors. There is yet a third variable called the failsafe error counter that is incremented each time a failsafe shut down occurs. The failsafe error counter can be read with yet another command. Lastly, both motors can be restarted by simply sending another command that sets the speed of either motor.

Finally, there is one other variable that is shared between the two motors called the prescaler. The prescaler is 3-bits wide and controls duty cycle width of the pulses are sent to the motor. The table below summarizes the prescaler to duty cycle relationship:

Prescaler	<b>Duty Cycle Width</b>
000	.5μS
001	1μS
010	2μS
011	4μS
100	8μS
101	16μS
110	32μS
111	64μS

The DualMotor1Amp commands are summarized in the table below:

Command	Send/			B	yte	Va	lue			Discussion
	Receive	7	6	5	4	3	2	1	0	
Set Quick	Send	0	0	h	h	h	h	d	m	Set motor $m$ speed to $hhhh$ $hhhh$ and direction to $d$ (0=forward, 1=backward).
Set Low	Send	0	1	l	l	l	l	d	m	Set low order 4 bits of motor <i>m</i> speed to <i>ll</i> and direction to <i>d</i> (0=forward, 1=backward).
	Send	1	0	0	0	0	0	0	m	Set the ramp for motor <i>m</i> to
Set Ramp	Send	r	r	r	r	r	r	r	r	rrrrrrr (00000000=no ramp (default)).
Set Failsafe	Send	1	0	0	0	0	0	1	0	Set the failsafe delay variable to ffffffff (00000000=off (default)).
Set Fallsale	Send	f	f	f	f	f	f	f	f	
Reset Failsafe	Send	1	0	0	0	0	0	1	1	Reset the failsafe counter to the failsafe delay variable.
Set Speed	Send	1	0	0	0	0	1	d	m	Set motor <i>m</i> to speed <i>ssssssss</i> and
Set Speed	Send	S	S	S	S	S	s	S	s	direction to $d$ .

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				1	1	1	1	1		
Set Mode	Send	1	0	0	0	1	0	х	m	Set motor $m$ mode to $x$ (0=pulsed (default), 1=continuous).
Set Direction	Send	1	0	0	0	1	1	d	m	Set motor $m$ direction to $d$ (0=forward (default), 1=reverse).
Set Prescaler	Send	1	0	0	1	0	p	p	p	Set prescaler to <i>ppp</i> (000=fast, 111=slow (default)).
Read Failsafe	Send	1	0	0	1	1	0	0	0	Read the return the failsafe delay
Read Prescaler	Receive	f	f	f	f	f	f	f	f	variable ffffffff.  Read the return the prescaler ppp.
	Send	1	0	0	1	1	0	0	1	
Read Trescaler	Receive	0	0	0	0	0	p	p	p	Read the return the presence ppp.
Read Speed	Send	1	0	0	1	1	0	1	m	Read the return the speed ssssssss
Read Speed	Receive	S	S	S	S	S	S	S	S	for motor <i>m</i> .
	Send	1	0	0	1	1	1	0	m	Read the mode $x$ (0=pulsed,
Read Mode/Direction	Receive	0	0	0	0	0	0	х	d	1=continuous) and direction <i>d</i> (0=forward, 1=reverse) for motor <i>m</i> .  Read and return the ramp <i>rrrrrr</i>
D 1D	Send	1	0	0	1	1	1	1	m	
Read Ramp	Receive	r	r	r	r	r	r	r	r	for motor <i>m</i> .  Read and return the failsafe error
	Send	1	0	1	0	0	0	0	0	
Read Failsafe Errors	Receive	e	e	e	e	e	e	e	e	counter <i>eeeeeeee</i> . Reset the counter.
Read Failsafe	Send	1	0	1	0	0	0	0	1	Read and return the failsafe
Counter	Receive	с	c	c	c	c	с	c	c	counter <i>ccccccc</i> .  Read and return the actual speed
Dood Actual Coord	Send	1	0	1	0	0	0	1	m	
Read Actual Speed	Receive	e	e	e	e	e	e	e	e	for motor <i>m</i>
On/Off	Send	1	0	1	0	0	1	o	m	Set motor $m$ to condition $o$ (0=off 1=on)
Reset	Send	1	0	1	0	1	0	0	0	Reset the entire motor controller
Shared Commands	Send	1	1	1	1	1	c	c	c	Execute shared command ccc.

On power up, the DualMotor1Amp module sets all variables to zero. The motor modes default to pulsed forward.

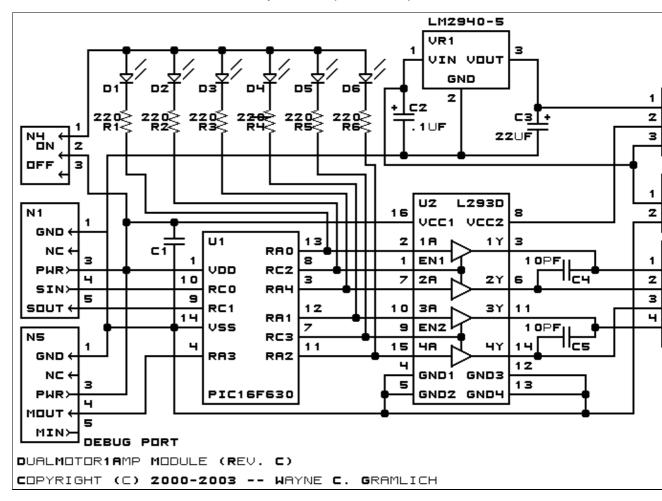
# 3. Hardware

The hardware consists of a circuit schematic and a printed circuit board.

# 3.1 Circuit Schematic

The schematic for the DualMotor1Amp module is shown below:

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The parts list kept in a separate file — <u>dualmotor1amp.ptl</u>.

#### 3.2 Printed Circuit Board

The printed circuit files are listed below:

dualmotor1amp back.png

The solder side layer.

dualmotor1amp front.png

The component side layer.

dualmotorlamp artwork.png

The artwork layer.

dualmotor1amp.gbl

The RS-274X "Gerber" back (solder side) layer.

dualmotor1amp.gtl

The RS-274X "Gerber" top (component side) layer.

dualmotor1amp.gal

The RS-274X "Gerber" artwork layer.

dualmotor1amp.drl

The "Excellon" NC drill file.

dualmotor1amp.tol

The "Excellon" NC drill rack file.

3.2 Printed Circuit Board

## 4. Software

The DualMotor1Amp software is available as one of:

### dualmotor1amp.ucl

The  $\mu CL$  source file.

#### dualmotor1amp.asm

The resulting human readable PIC assembly file.

#### dualmotor1amp.lst

The resulting human readable PIC listing file.

#### <u>dualmotor1amp.hex</u>

The resulting Intel<sup>®</sup> Hex file.

### 5. Issues

The following issues need to be considered:

- Consider using the TI 754410 instead of the LM293.
  - ◆ Pin 4 is RA3 which is read only. Move to another pin which can be written to. Copyright (c) 2000–2004 by Wayne C. Gramlich. All rights reserved.

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