

This is the Revision A version of the IRDistance8 Module. The status of this project is finished.

IRDistance8 Module (Revision A)

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1. Introduction

The IRDistance8 Module is used to connect up to 5 Sharp[®] GP2D12 IROD (InfraRed Optical Distance) measuring sensors. The GP2D12 module provides an analog voltage that is proportional to the distance (although not linearly.)

2. Programming

The IRDistance8 Module can enable zero, one or more of the AIROD's. For the ones that are enabled, it continuously reads the distance values. To conserve power, only one AIROD is powered up at a time.

The IRDistance8 Module supports Module Interrupt Protocol for those lines that are being used as inputs. The interrupt pending bit is set whenever the the formula:

$$L \& (\sim I) \mid H \& I \mid R \& (\sim P) \& I \mid F \& P \& (\sim I)$$

is non-zero, where:

- I is the current input bits XOR'ed with the complement mask (C)
- P is the previous value of I
- L is the low mask
- H is the high mask
- R is the raising mask
- F is the falling mask

and

- ~ is bit-wise complement
- | is bit-wise OR
- & is bit-wise AND

Once the interrupt pending bit is set, it must be explicitly cleared by the user.

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In addition to the common shared commands and the shared interrupt commands, the AnalogIn4 Module supports following commands:

Command	Send/ Receive	Byte Value								Discussion
		7	6	5	4	3	2	1	0	
Read Distance	Send	0	0	0	0	0	0	0	<i>b</i>	Read IROD <i>b</i> and respond with 8-bit value <i>ddddddd</i>
	Receive	<i>d</i>	<i>d</i>	<i>d</i>	<i>d</i>	<i>d</i>	<i>d</i>	<i>d</i>	<i>d</i>	
Read Binary Values	Send	0	0	0	0	0	0	1	0	Return the binary values <i>ab</i> (after XOR'ing with complement mask)
	Receive	0	0	0	0	0	0	<i>a</i>	<i>b</i>	
Read Raw Binary	Send	0	0	0	0	0	0	1	1	Return the raw binary values <i>ab</i> (no XOR with complement mask)
	Receive	0	0	0	0	0	0	<i>a</i>	<i>b</i>	
Reset	Send	0	0	0	0	0	1	0	0	Reset everything to zero
Read Enable Bit	Send	0	0	0	0	0	1	0	1	Read and return the enable bit <i>e</i>
	Receive	0	0	0	0	0	0	0	<i>e</i>	
Set Enable Bit	Send	0	0	0	0	0	1	1	<i>e</i>	Set enable bit to <i>e</i>
Read Complement Mask	Send	0	0	0	0	1	0	0	0	Return and return the complement mask <i>cccc</i>
	Receive	0	0	0	0	0	0	<i>c</i>	<i>c</i>	
Read High Mask	Send	0	0	0	0	1	0	0	1	Return and return the high mask <i>hh</i>
	Receive	0	0	0	0	0	0	<i>h</i>	<i>h</i>	
Read Low Mask	Send	0	0	0	0	1	0	1	0	Return and return the low mask <i>ll</i>
	Receive	0	0	0	0	0	0	<i>l</i>	<i>l</i>	
Read Raising Mask	Send	0	0	0	0	1	0	1	1	Return and return the raising mask <i>rr</i>
	Receive	0	0	0	0	0	0	<i>r</i>	<i>r</i>	
Read Falling Mask	Send	0	0	0	0	1	1	0	0	Return and return the falling mask <i>ff</i>
	Receive	0	0	0	0	0	0	<i>f</i>	<i>f</i>	
Read High Threshold	Send	0	0	0	1	0	0	0	<i>b</i>	Read and return high threshold for pin <i>b</i> of <i>hhhhhhh</i>
	Receive	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	
Read Low Threshold	Send	0	0	0	1	0	0	1	<i>b</i>	Read and return low threshold for pin <i>bb</i> of <i>lllllll</i>
	Receive	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	
Set High Threshold	Send	0	0	0	1	0	1	0	<i>b</i>	Set high threshold for pin <i>b</i> to <i>hhhhhhh</i>
	Send	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	<i>h</i>	
Set Low Threshold	Send	0	0	0	1	0	1	1	<i>b</i>	Set low threshold for pin <i>b</i> to <i>lllllll</i>
	Send	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	<i>l</i>	
Set Complement Mask	Send	0	0	1	0	0	0	<i>c</i>	<i>c</i>	Set complement mask to <i>cc</i>
Set High Mask	Send	0	0	1	0	0	1	<i>h</i>	<i>h</i>	Set high mask to <i>hh</i>
Set Low Mask	Send	0	0	1	0	1	0	<i>l</i>	<i>l</i>	Set low mask to <i>ll</i>
Set Raising Mask	Send	0	0	1	0	1	1	<i>r</i>	<i>r</i>	Set raising mask to <i>rr</i>
Set Falling Mask	Send	0	0	1	1	0	0	<i>f</i>	<i>f</i>	Set falling mask to <i>ff</i>
Read Interrupt Bits	Send	1	1	1	0	1	1	1	1	Return the interrupt pending bit <i>p</i> and the interrupt enable bit <i>e</i> .
	Receive	0	0	0	0	0	0	<i>e</i>	<i>p</i>	
Set Interrupt Commands	Send	1	1	1	1	0	<i>c</i>	<i>c</i>	<i>c</i>	Set Interrupt Command <i>ccc</i> .
Shared Commands	Send	1	1	1	1	1	<i>c</i>	<i>c</i>	<i>c</i>	Execute common shared command <i>ccc</i>

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The RS-272X "Gerber" back (solder side) layer.

irdistance8.gtl

The RS-272X "Gerber" top (component side) layer.

irdistance8.gal

The RS-272X "Gerber" artwork layer.

irdistance8.drl

The "Excellon" NC drill file.

irdistance8.tol

The "Excellon" tool rack file.

4. Software

The software for the IRDistance8 is listed below:

irdistance8.ucl

The μ CL file for IRDistance8.

irdistance8.asm

The assembly file for IRDistance8.

irdistance8.hex

The Intel[®] Hex file.

irdistance8.lst

The listing file for IRDistance8.

5. Issues

The following issues need to be addressed:

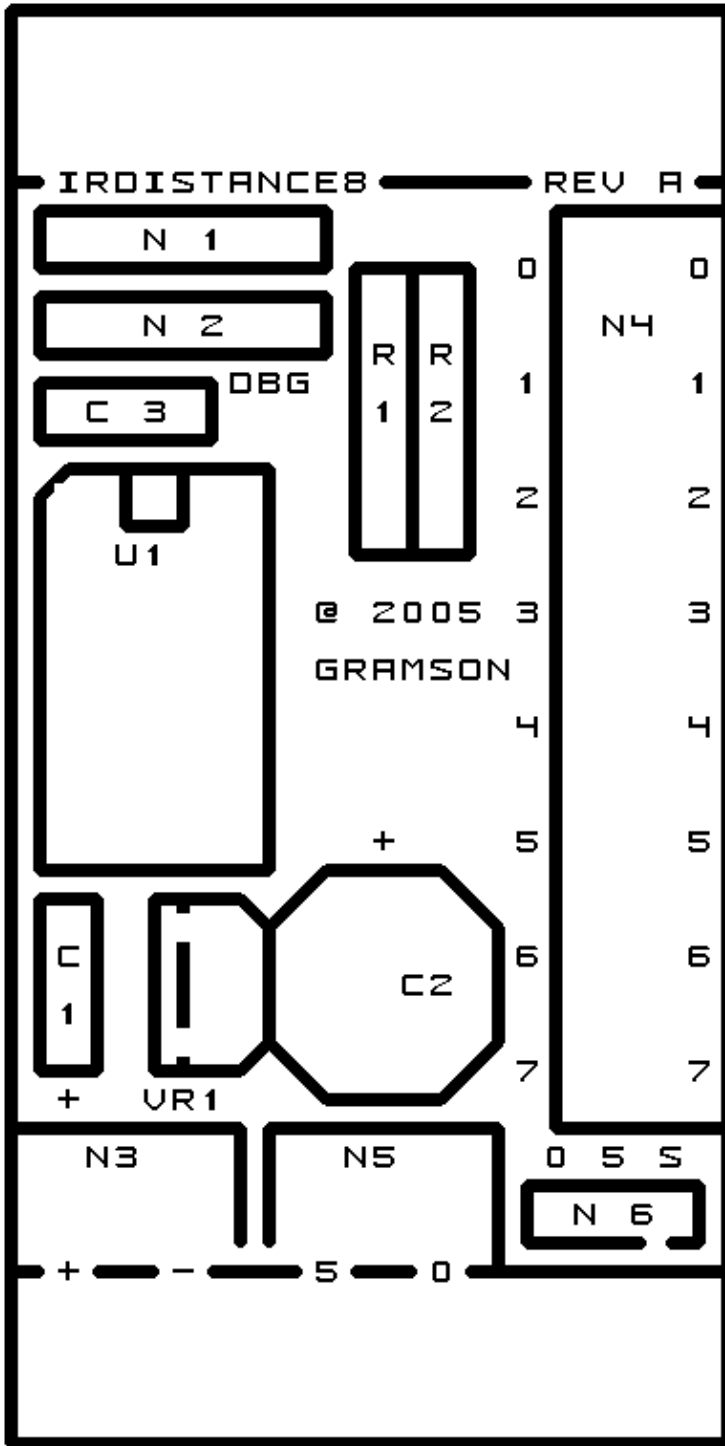
- U1 needs to be moved up by at least .05", preferably .10". The chip interferes with VR1.
- Contemplate rotating VR1 by 90 degrees to provide more space.
- Contemplate moving C2 up between N4 and U1 to provide more space for VR1. C2 can be moved right by .05".
- Think about moving R1 and R2 up a little.

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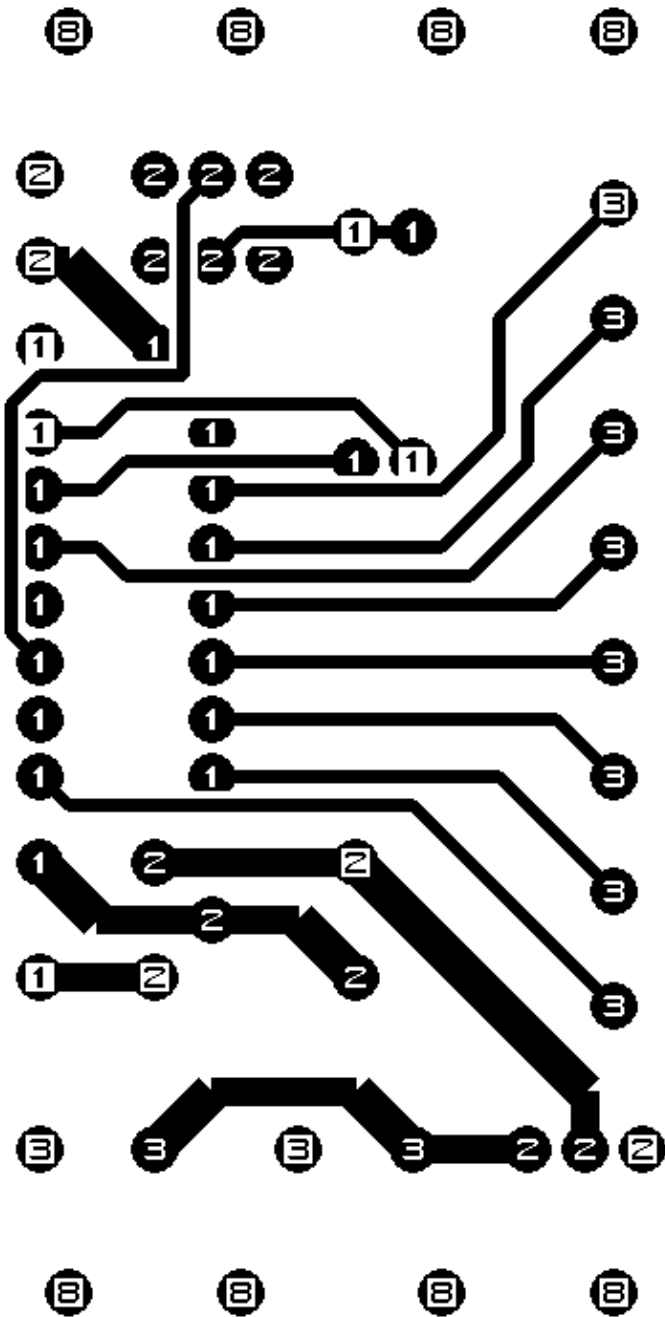
A. Appendix A: Parts List

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# Parts list for IRDistance8 RoboBrix (Rev. A)
#
C1: Capacitor470nF - .47 uF Tantalum Capacitor [Jameco: 33531]
C2: Capacitor3300uF - 3300 uF Tantalum Capacitor [Jameco: 198897]
C3: Capacitor100nF - .1 uF Ceramic Capacitor [Jameco: 25524]
N1: Header1x5.Slave - 1x5 Male Header [5/40 Jameco: 160881]
N2: Header1x5.Debug2 - 1x5 Male Header [5/40 Jameco: 160881]
N3: TerminalStrip2.Power - 2 Junction Terminal Strip [Jameco: 189675]
N4: TerminalStrip8 - 8 Junction Terminal Strip [4 Jameco: 189675]
N5: TerminalStrip2.Power - 2 Junction Terminal Strip [Jameco: 189675]
N6: Header1x3.Servo - 1x3 Male Header [3/40 Jameco: 160881]
R1-2: Resistor10K - 10K Ohm 1/4 Watt Resistor [Jameco: 29911]
U1: PIC16F688.IRDistance8 - Microchip PIC16C688 [Digkey: PIC16F688-I/P-ND]
VR1: LM2940CG-5 - 5 Volt Low Dropout Voltage Regulator [Jameco: 107182]
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B. Appendix B: Artwork Layer



C. Appendix C: Back (Solder Side) Layer



D. Appendix D: Front (Component Side) Layer

